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# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT** ( Not for submission under 37 CFR 1.99)



Application Number	10675778
Filing Date	2003-09-30
First Named Inventor	DeWitt, Jr. et al.
Art Unit	2183
Examiner Name	Cody, Dillon J.
Attorney Docket Number	AUS920030480US1

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	1	6542985		2003-04-01	Johnson et al.	
	2	4316245		1982-02-16	Luu et al.	
	3	7181723		2007-02-20	Luk et al.	
	4	6961925		2005-11-01	Callahan, II et al.	
	5	5950003		1999-09-30	Kaneshiro et al.	
	6	5212794		1993-05-18	Pettis et al.	
	7	5659679		1997-08-19	Alpert et al.	
	8	6006033		1999-12-21	Heisch	

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	9	6119075		2000-09-12	Dean et al.	
	10	6189141	B1	2001-02-13	Benitez et al.	
	11	6237141	B1	2001-05-22	Holzle et al.	
	12	6351844	B1	2002-02-26	Bala	
	13	6505292	B1	2003-01-07	Witt	
	14	6631514	B1	2003-10-07	Le	
	15	5794028		1998-08-11	Tran	
	16	5950009		1999-09-07	Bortnikov et al.	
	17	5950003		1999-09-07	Kaneshiro et al.	
	18	5581778		1996-12-03	Chin et al.	
	19	5752062		1998-05-12	Gover et al.	

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20	6446029	B1	2002-09-03	Davidson et al.	
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	2	20020073406	A1	2002-06-13	Gove	
	3	20030014741	A1	2003-01-16	Megiddo et al.	
	4	20020157086	A1	2002-10-24	Lewis et al.	
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	1	RAMIREZ et al., "The Effect of Code Reordering on Branch Prediction", Proceedings of the International Conference on Parallel Architectures and Compilation Techniques, October 2000, pp. 189-198.	<input type="checkbox"/>
	2	YANG et al., "Improving Performance by Branch Reordering", Proceedings of the ACM SIGPLAN 1998 Conference on Programming Language Design and Implementation, Montreal Canada, 1008, pp. 130-141.	<input type="checkbox"/>
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	4	CONTE et al., "Using Branch Handling Hardware to Support Profile-Driven Optimization", Proceedings of the 27th Annual International Symposium on Microarchitecture, San Jose CA, 1994, pp. 12-21.	<input type="checkbox"/>
	5	FISHER, "Trace Scheduling: A Technique for Global Microcode Compaction", IEEE Transactions on Computers, Vol. C30, No. 7, July 1981, pp. 478-490.	<input type="checkbox"/>
	6	CHANG et al., "Using Profile Information to Assist Classic Code Optimizations", Software Pract. Exper. 21, December 1991, pp. 1301-1321.	<input type="checkbox"/>
	7	SCHMIDT et al., "Profile-Directed Restructuring of Operating System Code", IBM Systems Journal, 1998, Vol. 37, No. 2, pp. 270-297.	<input type="checkbox"/>
	8	AHO et al., "Compilers: Principles, Techniques, and Tools", Addison-Wesley, 1988, pp. 488-497.	<input type="checkbox"/>
	9	INTEL, "Intel IA-64 Architecture Software Developer's Manual", Revision 1.1, Vol. 4, No. 245320.002, July 2001.	<input type="checkbox"/>

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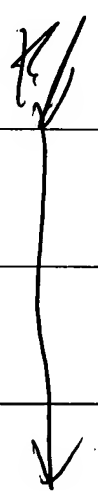
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	2	6928521	B1	2005-08-09	Burton et al.	
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	4	5875294		1999-02-23	Roth et al.	
	5	6073109		2000-06-06	Flores et al.	
	6	6871298	B1	2005-03-22	Cavanaugh et al.	
	7	5555432		1996-09-10	Hinton et al.	
	8	6145077		2000-11-07	Sidwell et al.	

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	9	6865666	B2	2005-03-08	Yoshida et al.	
	10	5590352		1996-12-31	Zuraski et al.	
	11	6199204	B1	2001-03-06	Donohue	
	12	6202207	B1	2001-03-13	Donohue	

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	3	20030041096	A1	2003-02-27	Johnson	

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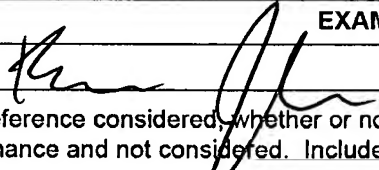
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


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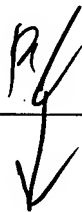
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	1	5657253		1997-08-12	Dreyer et al.	
	2	5991708		1999-11-23	Levine et al.	
	3	5564015		1996-10-08	Bunnell	
	4	5450349		1995-09-12	Brown et al.	
	5	5987598		1999-11-16	Levine et al.	
	6	6009514		1999-12-18	Henzinger et al.	
	7	6112317		2000-08-29	Berc et al.	
	8	5896538		1999-04-20	Blandy et al.	

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	9	5355487		1994-10-11	Keller et al.	
	10	6948032	B2	2005-09-20	Kadambi et al.	

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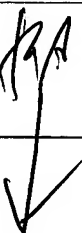
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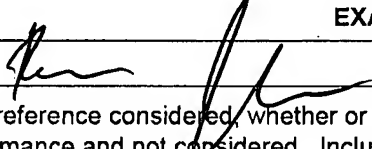
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	2	AMMONS et al., "Exploiting Hardware Performance Counters with Flow and Context Sensitive Profiling", 1997, ACM, pp. 85-96.					<input type="checkbox"/>

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	4	5937437		1999-08-10	Roth et al.	
	5	6070009		2000-05-30	Dean et al.	
	6	6381679	B1	2002-04-30	Matsubara et al.	
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9	6973542	B1	2005-12-06	Schmuck et al.	
10	6782454	B1	2004-08-24	Damron	
11	5652858		1997-06-29	Okada et al.	
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	2	5940618		1999-08-17	Blandy et al.	
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	5	6026235		2000-02-15	Shaughnessy	
	6	5950003		1999-09-07	Kaneshiro et al.	
	7	6594820	B1	2003-07-15	Ungar	

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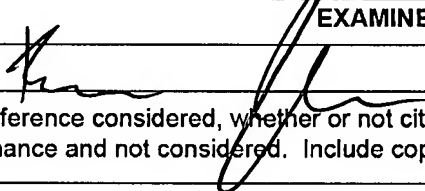
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Filing Date	2003-09-30
First Named Inventor	DeWitt, Jr. et al.
Art Unit	2183
Examiner Name	Cody, Dillon J.
Attorney Docket Number	AUS920030480US1

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	2	6185671	B1	2001-02-06	Pentovski et al.	
	3	6247113	B1	2001-06-12	Jaggar	
	4	6460135	B1	2002-10-01	Suganuma	
	5	6842850	B2	2005-01-11	Ganapathy et al.	
	6	5857097		1999-01-05	Henzinger et al.	
	7	5745770		1998-04-28	Thangadurai et al.	

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	1	20030126590	A1	2003-07-03	Burrows et al.	
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	1	MERTEN et al., "A Hardware Driven Profiling Scheme for Identifying Program Hot Spots to Support runtime Optimization", IEEE 1999, pp. 136-147.	<input type="checkbox"/>

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Examiner Name	Cody, Dillon J.
Art Unit	2183
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Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AA	5805879	09-08-1998	Hervin et al.	
AB	6647301 B2	11-11-2003	Sederlund et al.	
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AA	5051944	09-24-1991	Fetterolf et al.	
AB	5142634	08-25-1992	Fite et al.	
AC	5394529	02-28-1995	Brown, III et al.	
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AL	JEONG et al., "Cost Sensitive Cache Replacement Algorithms", June 2002, Second Workshop on Caching, Coherence and Consistency, NY, pp. 1-14.

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	1	5966538		1999-10-12	Granston et al.	
	2	5940618		1999-08-17	Blandy et al.	
	3	6189142	B1	2001-02-13	Johnston et al.	
	4	5768500		1998-06-16	Agrawal et al.	
	5	6026235		2000-02-15	Shaughnessy	
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	1	20020073406	A1	2002-06-13	Gove	
	2	20030066055	A1	2003-04-03	Spivey	
	3	20030131343	A1	2002-07-10	French et al.	
	4	20040268316	A1	2004-12-30	Fisher et al.	

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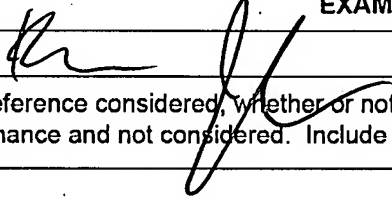
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